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Sheet 1 of 1

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INFORMATION DISCLOSURE CITATION
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U.S. PATENT DOCUMENTS

Ex. Int	Document Number	Date	Name	Class	Sub- class	Filing Date If Approp.
AA	5 0 5 5 8 0 2	04/1990	HIETALA ET AL	331	16	
AB	5 0 7 0 3 1 0	08/1990	HIETALA ET AL	331	1 A	
AC						
AD						
AE						
AF						

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Sub- class	Trans.	
						Yes	No
AG	0 6 6 1 8 1 6 B 1	12/1994	EUROPE	H03L	7/187		X
AH	0 5 6 3 4 0 0 A 1	10/1992	EUROPE	H03L	7/18		X
AI							
AJ							
AK							

OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)

AL	LIZHONG SUN ET AL; "REDUCED COMPLEXITY, HIGH PERFORMANCE DIGITAL DELTA-SIGMA MODULATOR FOR FRACTIONAL-N FREQUENCY SYNTHESIS", PROCEEDINGS OF THE 1999 IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, 1999, 'EN LIGNE, VOL. 2, 5/30/1999, PAGES 152-155, XP002168214.
AM	SHERKLING I; "SIGMA DELTA MODULATOR WITH A RANDOM OUTPUT BIT STREAM OF SPECIFIED 1/0 AVERAGE RATIO", MOTOROLA TECHNICAL DEVELOPMENTS, US, MOTOROLA INC. SCHAUMBURG, ILLINOIS, VOL. 18, 3/1/1993, PAGES 156-160, XP000349609.
AN	

Examiner *Mehur Ramakrishna*

Date Considered 12-14-01

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with M 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.